modular arithmetic, overflow Wellesley CS 240 13 1101 11 1011 14 1111 0000 0001 1110 13 2 +5 + 0101+ 2 + 0010 1101 0010 12 3 4-bit 1100 0011 **Integer Representation** unsigned 1011 integers 0100 11 4 1010 0101 10 0110 5 1001 1000 0111 g Representation of integers: unsigned and signed Modular arithmetic and overflow **x+y** in *n*-bit unsigned arithmetic is in math Sign extension Shifting and arithmetic unsigned overflow = Multiplication Casting Unsigned addition overflows if and only if 1 (4-bit) two's complement sign-magnitude signed integer representation Most-significant bit (MSB) is sign bit 0 means non-negative 1 means negative 1 1  $\mathbf{O}$ 1  $= 1 \times -2^{3} + 0 \times 2^{2} + 1 \times 2^{1} + 1 \times 2^{0}$ Remaining bits are an unsigned magnitude -**2**<sup>3</sup> **2**<sup>2</sup> 21 20 8-bit sign-magnitude: Anything weird here? 0000000 represents \_ **Arithmetic?** Example: 01111111 represents 4-bit two's complement integers: 4 - 3 != 4 + (-3)minimum = 10000101 represents 00000100 +10000011 10000000 represents maximum = Zero? 6 8



