

Procedures and the Call Stack

Topics

- Procedures
- Call stack
- Procedure/stack instructions
- Calling conventions
- Register-saving conventions

Why Procedures?

Why functions? Why methods?

```
int contains_char(char* haystack, char needle) {
    while (*haystack != '\0') {
        if (*haystack == needle) return 1;
        haystack++;
    }
    return 0;
}
```

Procedural Abstraction

Call Chain

```
yoo (...)
{
    .
    .
    who ();
    .
    .
}
```

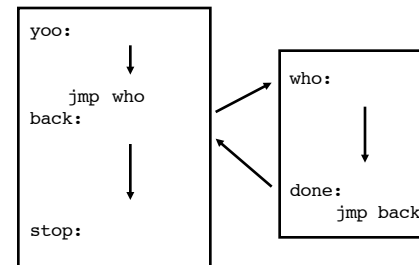
```
who (...)
{
    . . .
    ru ();
    . . .
    ru ();
    . . .
}
```

```
ru (...)
{
    .
    .
    .
}
```

Example Call Chain



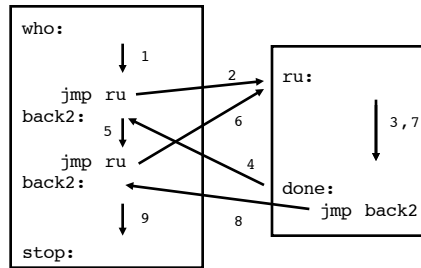
First Try (broken)



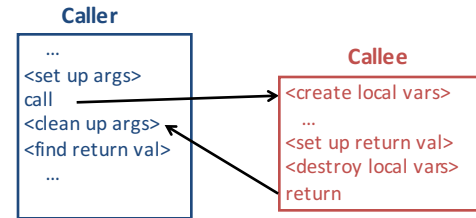
What if I want to call a function multiple times?

First Try (broken)

What if I want to call a function multiple times?



Procedure Calls: Lots to do!



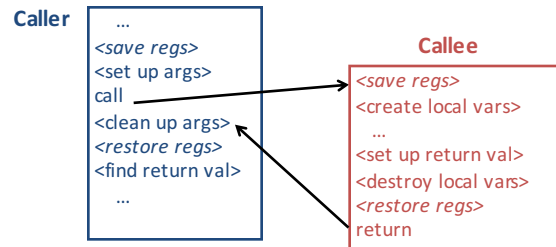
Callee must find **arguments** and **return address**, store local vars

Caller must find **return value**

Caller and **Callee** run on same CPU, use same registers...

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Procedure *Calling Convention*



Rules about where to save/find things for call/return.

Will see the convention for **IA32/Linux/GCC** in detail

Details vary per architecture, OS, maybe compiler

Why do we need a convention?

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Memory Layout

Addr	Perm	Contents	Managed by	Initialized
2^N-1 ↑	RW	Procedure context	Compiler	Run-time
	RW	Dynamic data structures	Programmer, malloc/free, new/GC	Run-time
	RW	Global variables/ static data structures	Compiler/ Assembler/Linker	Startup
	R	String literals	Compiler/ Assembler/Linker	Startup
0	X	Instructions	Compiler/ Assembler/Linker	Startup

IA32 Call Stack

Region of memory
Managed with stack discipline

`%esp` register holds lowest stack address
= address of "top" element.

Stack Pointer: `%esp`
(not extra-sensory perception)

Stack "Bottom"

Stack "Top"

higher addresses

stack grows toward lower addresses

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IA32 Call Stack: Push

`pushl Src`

Stack "Bottom"

Stack "Top"

Stack Pointer: `%esp`

higher addresses

stack grows toward lower addresses

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IA32 Call Stack: Push

`pushl Src`

1. Fetch value from `Src`
2. Decrement `%esp` by 4 (*why 4?*)
3. Store value at new address given by `%esp`

Stack "Bottom"

Stack "Top"

Stack Pointer: `%esp`

higher addresses

stack grows toward lower addresses

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IA32 Call Stack: Pop

`popl Dest`

Stack "Bottom"

Stack "Top"

Stack Pointer: `%esp`

higher addresses

stack grows toward lower addresses

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IA32 Call Stack: Pop

popl Dest

1. Load value from address %esp
2. Write value to Dest
3. Increment %esp by 4

Stack "Bottom" (higher addresses)

Stack "Top" (lower addresses)

Stack Pointer: %esp

stack grows toward lower addresses

Those bits are still there; we're just not using them.

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Procedure Control Flow Instructions

Stack supports procedure call and return.

Procedure call: call label

1. Push return address on stack
2. Jump to label

Return address: Address of instruction after call. Example:

804854e:	e8 3d 06 00 00	call 8048b90 <main>
8048553:	50	pushl %eax

Return address = 0x8048553

next instruction just happens to be a push could be anything

Procedure return: ret

1. Pop return address from stack
2. Jump to address

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Procedure Call Example #1

804854e:	e8 3d 06 00 00	call 8048b90 <main>
8048553:	50	pushl %eax

call 8048b90

0x110

0x10c

0x108 123

%esp 0x108

%eip 0x804854e

%eip = instruction pointer = program counter

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Procedure Call Example #2

804854e:	e8 3d 06 00 00	call 8048b90 <main>
8048553:	50	pushl %eax

call 8048b90

0x110

0x10c

0x108 123

0x104 0x8048553

%esp 0x104

%eip 0x8048553

%eip = instruction pointer = program counter

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Procedure Call Example #3

```

804854e: e8 3d 06 00 00 call 8048b90 <main>
8048553: 50          pushl %eax
    
```

call 8048b90 PC-relative address just like jumps... (chosen by compiler; there's also an absolute call)

%eip: program counter

Procedure Return Example #4

```

8048591: c3          ret
    
```

%eip: program counter

Call Chain Example

```

yoo (...)
{
  .
  .
  .
  who ();
  .
  .
}

who (...)
{
  .
  .
  .
  amI ();
  .
  .
  .
  amI ();
  .
  .
}

amI (...)
{
  .
  .
  .
  amI ();
  .
  .
}
    
```

Example Call Chain

```

yoo
  ↓
who
  ↓ ↘
amI  amI
  ↓
amI
  ↓
amI
    
```

Procedure amI is recursive (calls itself)

Stack Frames

Contents

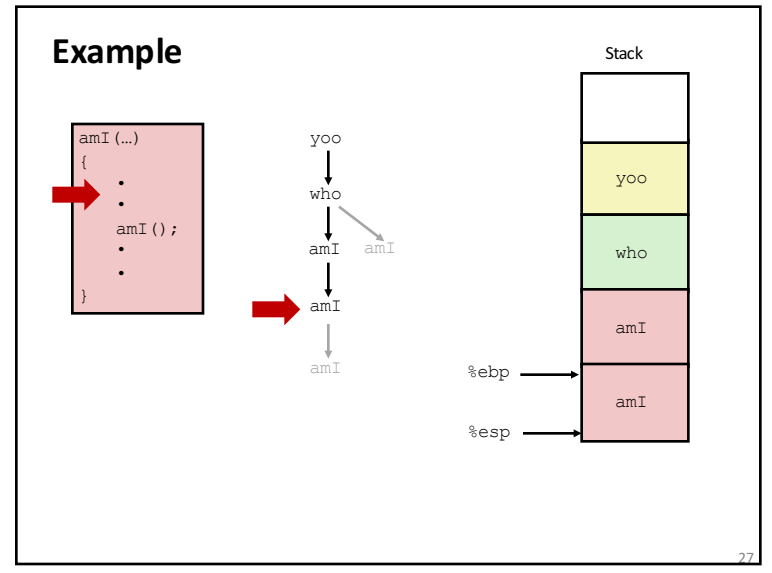
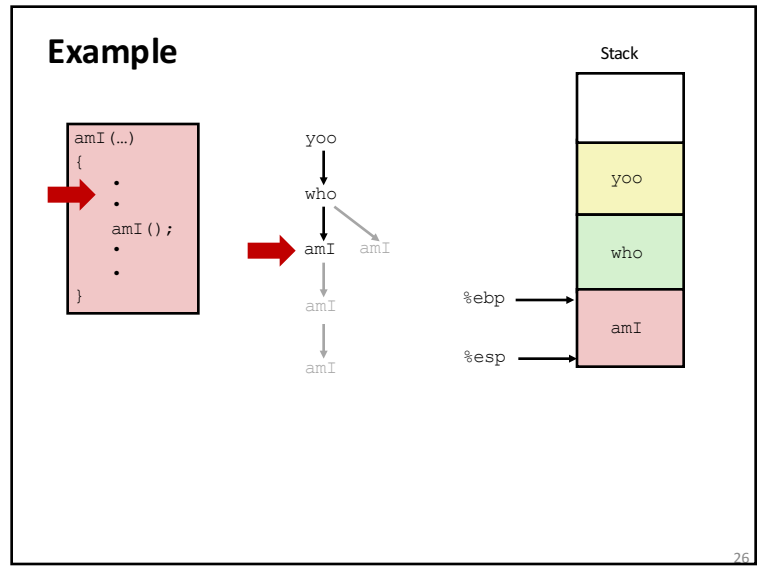
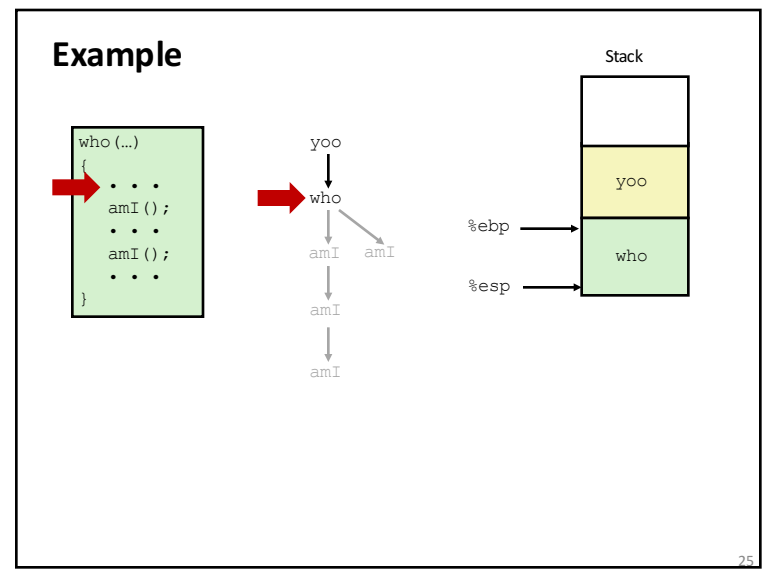
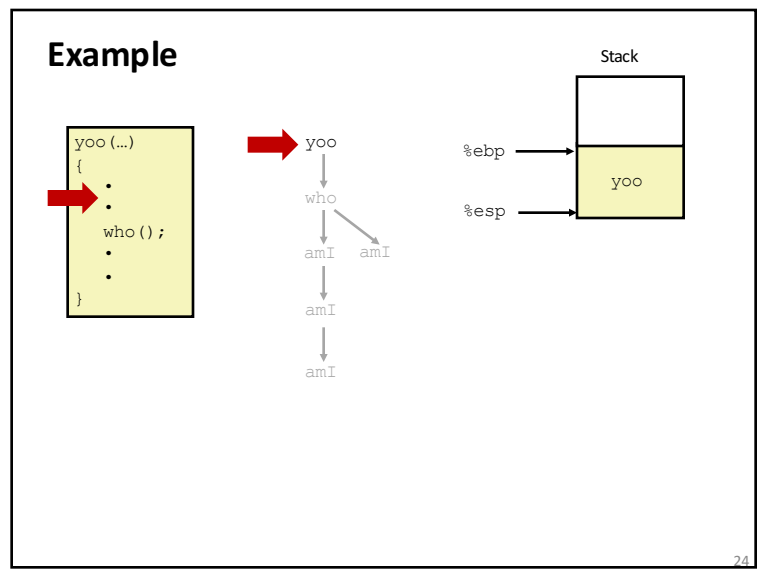
- Local variables
- Function arguments
- Return information
- Temporary space

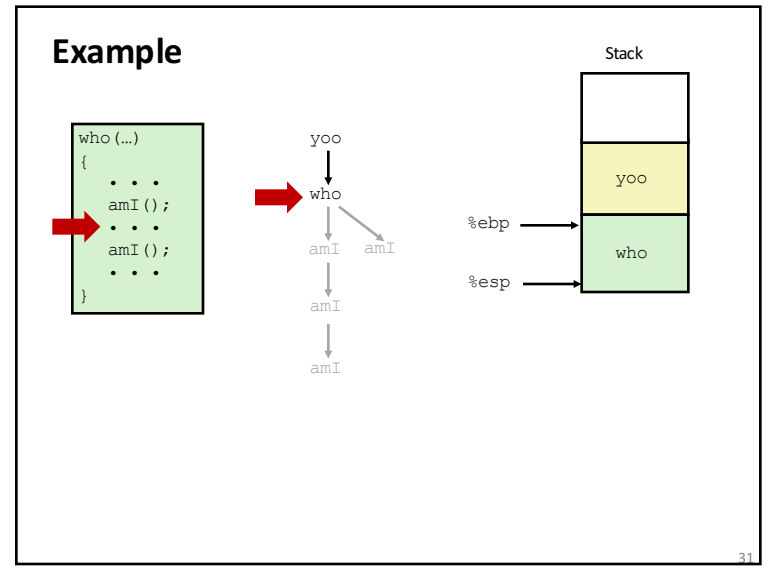
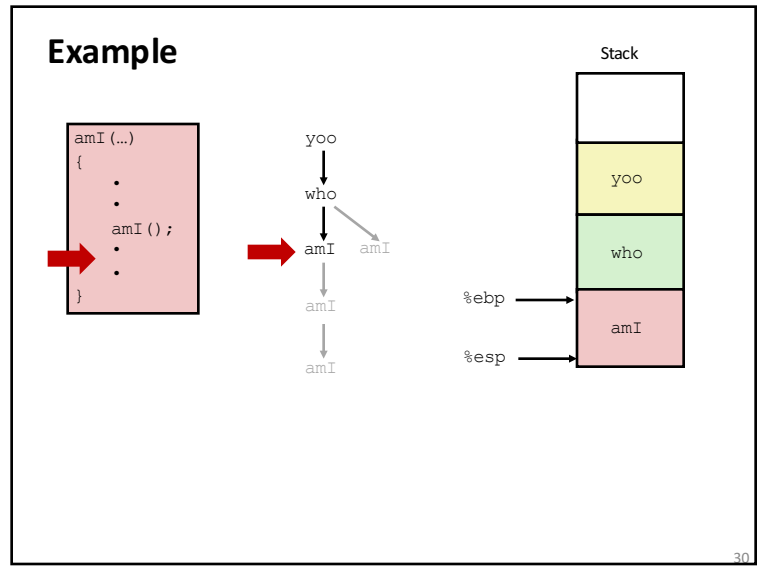
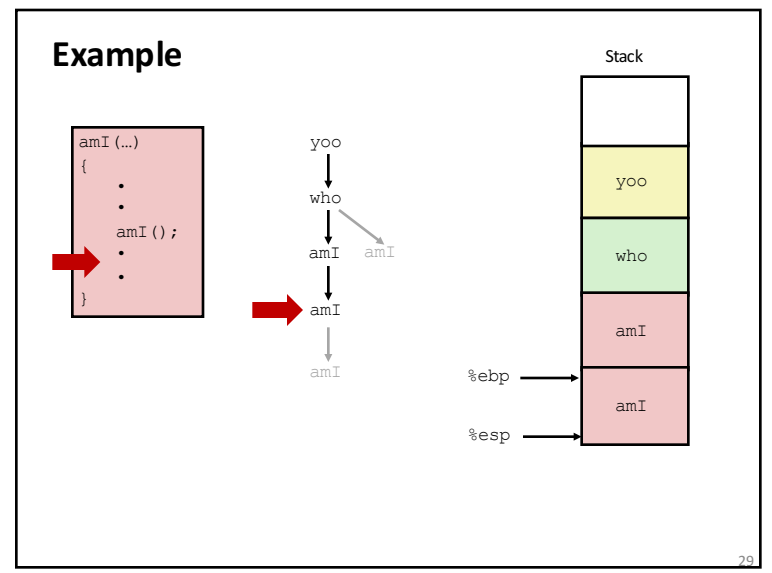
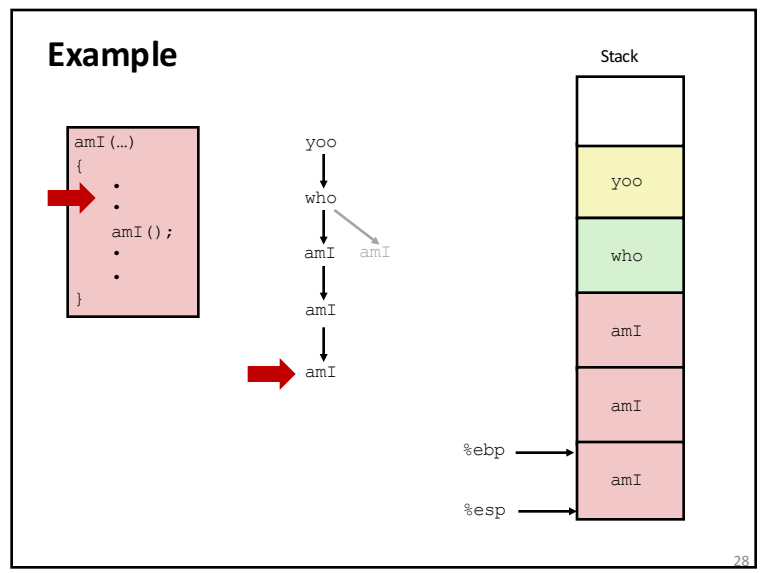
Management

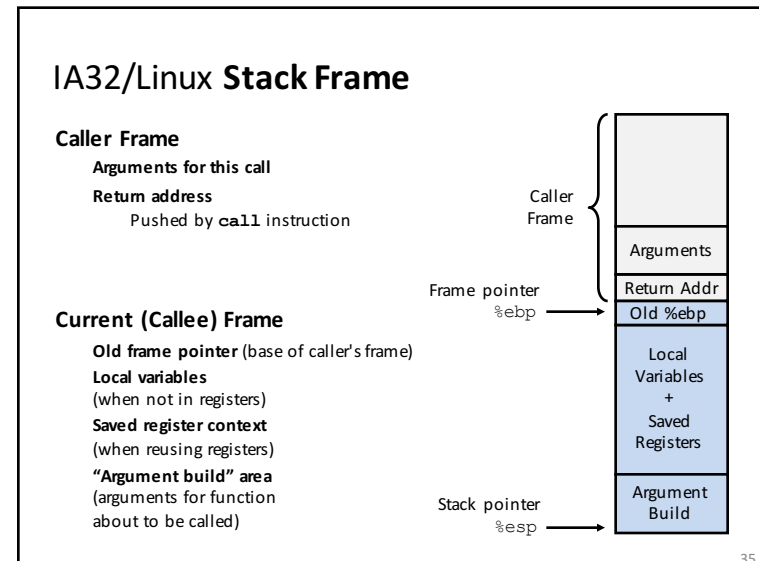
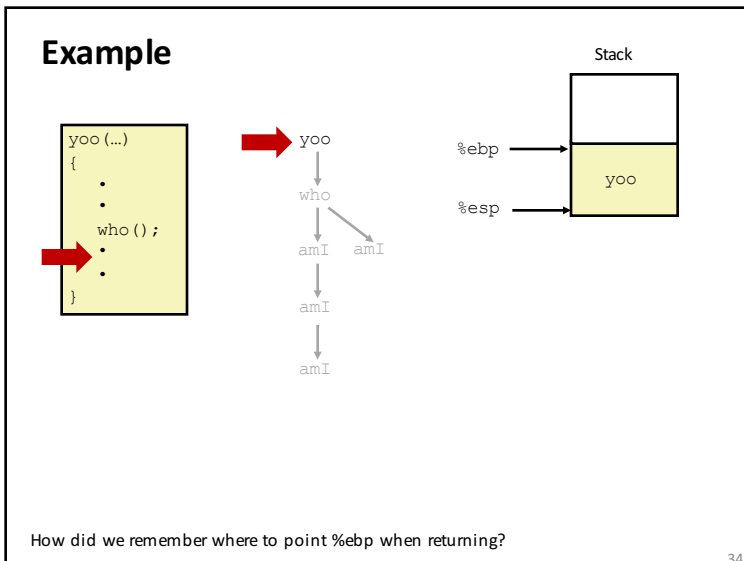
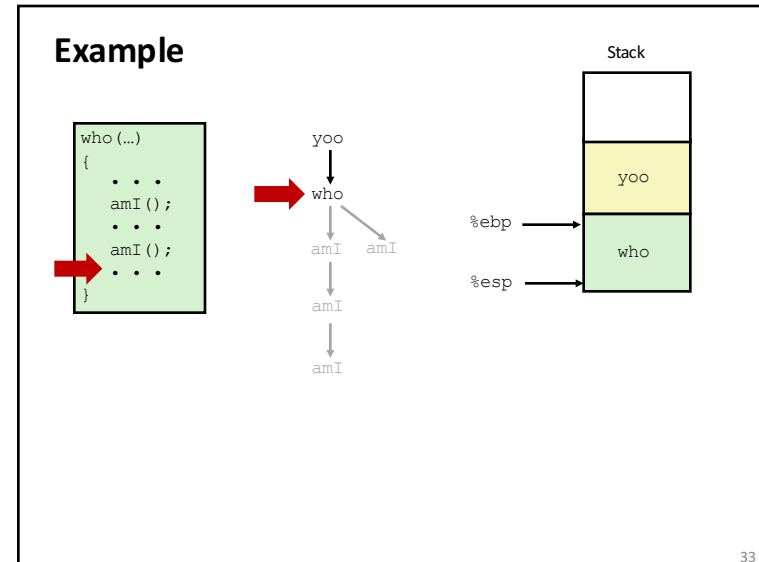
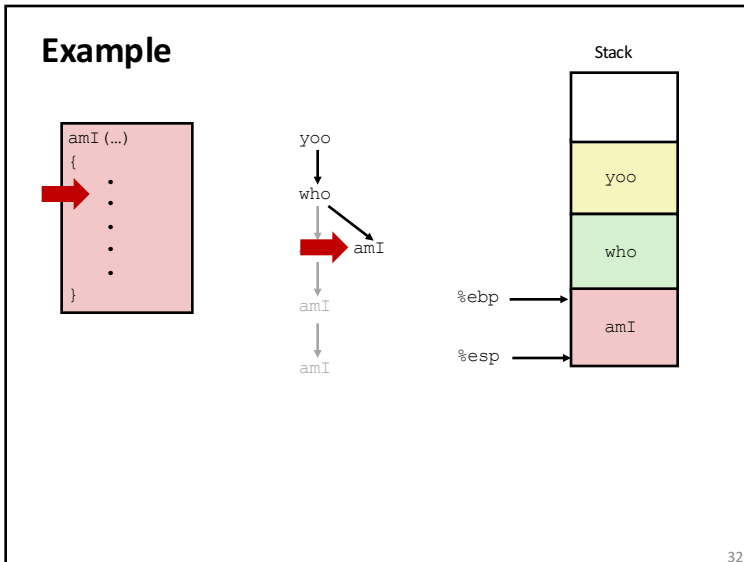
- Space allocated when procedure is entered
- "Set-up" code
- Space deallocated upon return
- "Finish" code

Why can't we just give every *procedure* a permanent chunk of memory to hold its local variables, etc?

Stack "Top"







Revisiting swap

```
int zip1 = 02481;
int zip2 = 98195;

void call_swap() {
    swap(&zip1, &zip2);
}
```

Calling swap from call_swap

```
call_swap:
...
pushl $zip2 # Global Var
pushl $zip1 # Global Var
call swap
...
```

```
void swap(int *xp, int *yp) {
    int t0 = *xp;
    int t1 = *yp;
    *xp = t1;
    *yp = t0;
}
```

Resulting Stack

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Revisiting swap

```
void swap(int *xp, int *yp) {
    int t0 = *xp;
    int t1 = *yp;
    *xp = t1;
    *yp = t0;
}
```

```
swap:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
    } Set Up

    movl 12(%ebp),%ecx
    movl 8(%ebp),%edx
    movl (%ecx),%eax
    movl (%edx),%ebx
    movl %eax, (%edx)
    movl %ebx, (%ecx)
    } Body

    movl -4(%ebp),%ebx
    movl %ebp,%esp
    popl %ebp
    ret
    } Finish
```

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swap Setup #1

Entering Stack

Resulting Stack

```
swap:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
    } Set Up
```

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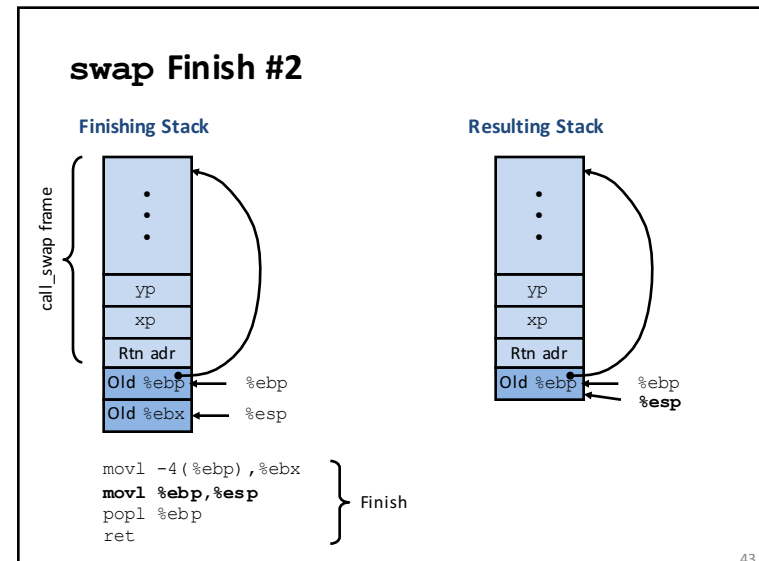
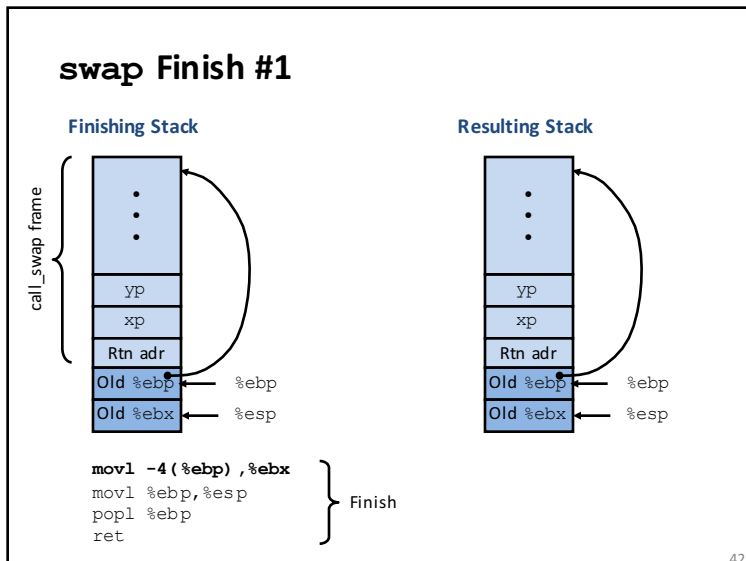
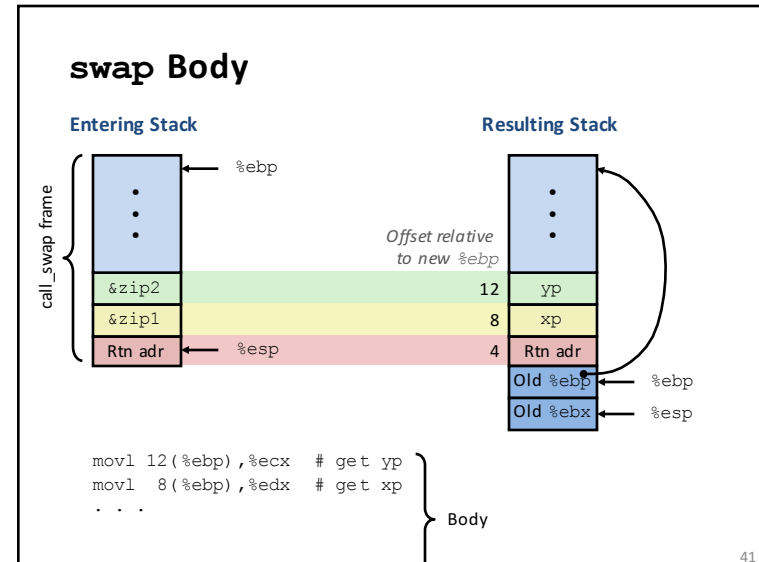
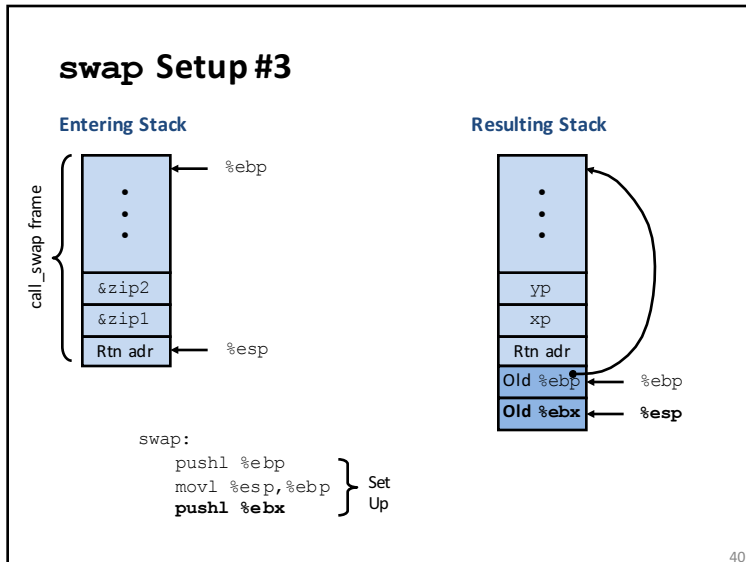
swap Setup #2

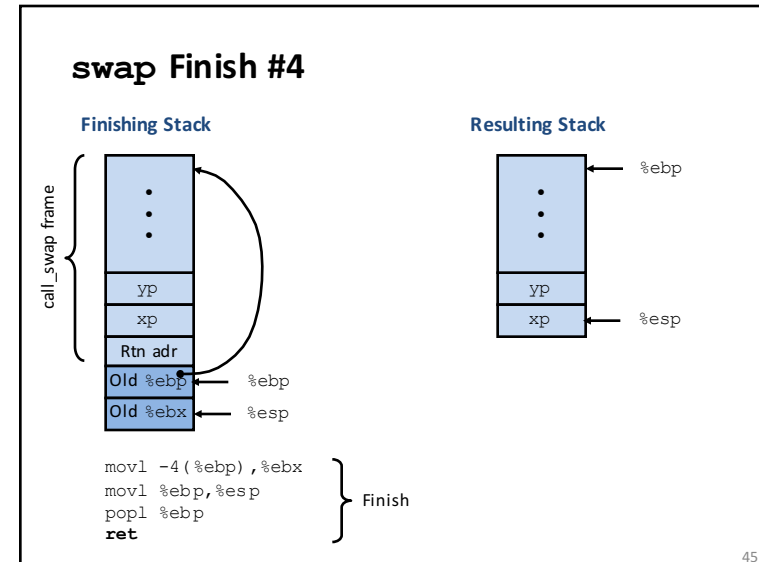
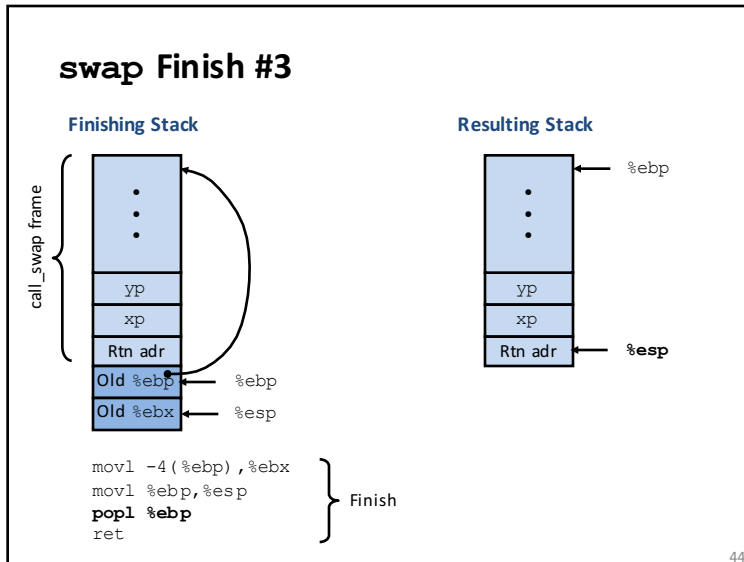
Entering Stack

Resulting Stack

```
swap:
    pushl %ebp
    movl %esp,%ebp
    pushl %ebx
    } Set Up
```

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Disassembled swap

```

080483a4 <swap>:
80483a4: 55      push   %ebp
80483a5: 89 e5   mov    %esp,%ebp
80483a7: 53      push   %ebx
80483a8: 8b 55 08 mov    0x8(%ebp),%edx
80483ab: 8b 4d 0c mov    0xc(%ebp),%ecx
80483ae: 8b 1a   mov    (%edx),%ebx
80483b0: 8b 01   mov    (%ecx),%eax
80483b2: 89 02   mov    %eax,(%edx)
80483b4: 89 19   mov    %ebx,(%ecx)
80483b6: 5b     pop    %ebx
80483b7: c9     leave  %ebp,%esp
80483b8: c3     ret
    
```

```

mov    %ebp,%esp
pop    %ebp
    
```

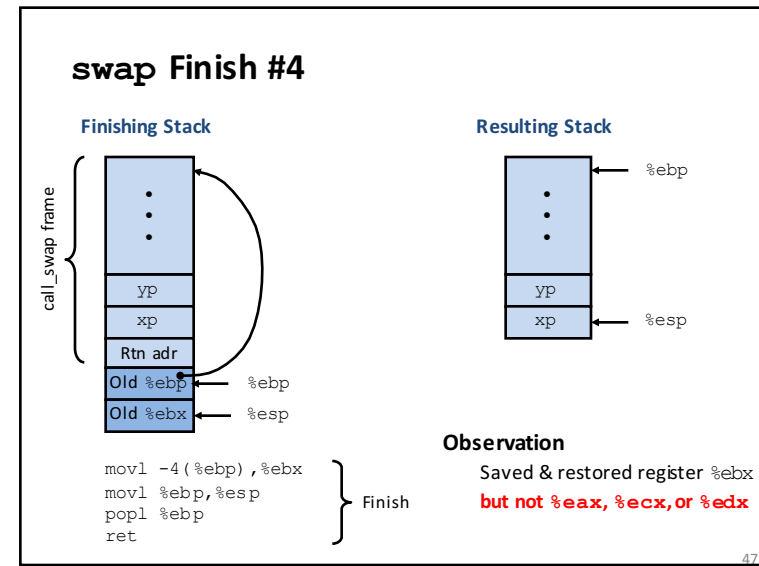
Calling Code

```

8048409: e8 96 ff ff ff call 080483a4 <swap>
804840e: 8b 45 f8      mov 0xfffff8(%ebp),%eax
    
```

relative address (little endian)

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A Puzzle

C function body:

```
*p = d;
return x - c;
```

Write the C function header, types, and order of parameters.

assembly:

```
movsbl 12(%ebp), %edx
movl 16(%ebp), %eax
movl %edx, (%eax)
movswl 8(%ebp), %eax
movl 20(%ebp), %edx
subl %eax, %edx
movl %edx, %eax
```

movsbl = move sign-extending a byte to a long (4-byte)
 movswl = move sign-extending a word (2-byte) to a long (4-byte)

Register Saving Conventions

When procedure yoo calls who:

yoo is the *caller*
 who is the *callee*

Will register contents still be there after a procedure call?

```
yoo:
    . . .
    movl $12345, %edx
    call who
    addl %edx, %eax
    . . .
    ret

who:
    . . .
    movl 8(%ebp), %edx
    addl $98195, %edx
    . . .
    ret
```

Register Saving Conventions

When procedure yoo calls who:

yoo is the *caller*
 who is the *callee*

Will register contents still be there after a procedure call?

Conventions

Caller Save

Caller saves temporary values in its frame **before calling**

Callee Save

Callee saves temporary values in its frame **before using**

IA32/Linux Register Saving Conventions

%eax, %edx, %ecx

Caller saves prior to call
 if needs values after call

%eax

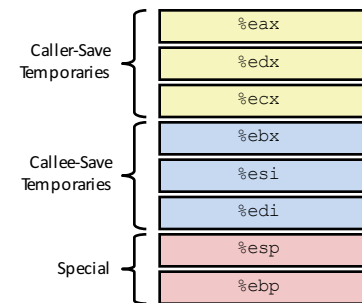
also used to **return**
 integer value

%ebx, %esi, %edi

Callee saves if will use

%esp, %ebp

special form of callee save
 restored to original values before returning



Example: Pointers to Local Variables

Top-Level Call

```
int sfact(int x) {
    int val = 1;
    s_helper(x, &val);
    return val;
}
```

Recursive Procedure

```
void s_helper
(int x, int *accum) {
    if (x <= 1) {
        return;
    } else {
        int z = *accum * x;
        *accum = z;
        s_helper (x-1, accum);
    }
}
```

```
sfact(3)      val = 1
s_helper(3, &val) val = 3
s_helper(2, &val) val = 6
s_helper(1, &val) val = 6.
```

Pass pointer to update location

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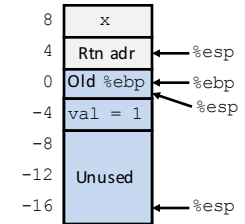
Creating & Initializing Pointer

```
int sfact(int x) {
    int val = 1;
    s_helper(x, &val);
    return val;
}
```

Variable **val** must be stored on stack
 Because: Need to create pointer to it
Compute pointer as -4 (%ebp)
Push on stack as second argument

Initial part of sfact

```
_sfact:
    pushl %ebp      # Save %ebp
    movl %esp,%ebp # Set %ebp
    subl $16,%esp  # Add 16 bytes
    movl 8(%ebp),%edx # edx = x
    movl $1,-4(%ebp) # val = 1
```

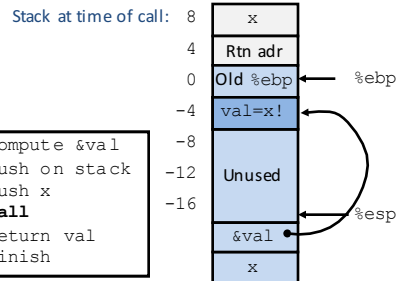


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Passing Pointer

```
int sfact(int x) {
    int val = 1;
    s_helper(x, &val);
    return val;
}
```

Variable **val** must be stored on stack
 Because: Need to create pointer to it
Compute pointer as -4 (%ebp)
Push on stack as second argument



Calling s_helper from sfact

```
leal -4(%ebp),%eax # Compute &val
pushl %eax         # Push on stack
pushl %edx         # Push x
call s_helper      # call
movl -4(%ebp),%eax # Return val
...               # Finish
```

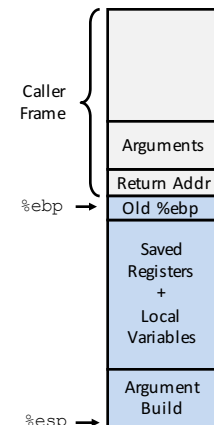
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IA32/Linux Procedure Summary

call, ret, push, pop

Stack discipline fits procedure call / return.*
 If P calls Q, Q returns before P, including recursion.

Conventions support arbitrary function calls.
 Safely store per-call values in local stack frame and callee-saved registers
 Push function arguments at top of stack before call
 Result returned in **%eax**



*take 251 to learn about languages where it doesn't quite.

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