CS 240 Circuits Assignment [83 points]

ID Number:

About how many hours did you spend actively working on this assignment?

Q1 Decoding a T-Shirt [10 points] Time spent on Q1: _____

1a [3 points] Message in base of flag

1b [7 points] Message in binary bits of the flag. Write down the bits and show your decoding details

Q2 Decoding a Unicode Message [12 points] Time spent on Q2:

2a [10 points] Show how to decode the Unicode message in the hex bytes 49 E2 99 A5 CF 80 21 to Unicode code points. Write the **message bits** corresponding to the hex bytes, distinguish **header** bits from **content** bits, and indicate **the number of bytes in each code point.** Show the **code points** determined by the content bits.

2b [2 points]. What would the message look like in a Unicode enabled application?

Q3 Universal Muxification of Gates [14 points] Time spent on Q3:				
3a NOT A (one 2:1 mux) [1]	3b. A AND B (one 2:1 mux) [1.5]	3c. A OR B (one 2:1 mux) [1.5]	
3d A NAND B (two 2:1 muxes) [2]		3e A NOR B (two	9 2:1 muxes) [2]	
3f A XOR B (two 2:1 muxes) [3]		3g A XOR B (one	e 2:4 decoder and one 2:1 mux) [3]	

4 Switching Network	[8 points]	Time spent on	Q4:
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Q5 Karna	augh Map	s [10 poi	nts] Time	spent on Q	5:
5a. Karnaugh Map [8 points]]		5b. Minimal sum of products expression [2 points]

Q6 vA	LUe [2	9 points]	Time spent	on Q6:	Draw circuits on next page, text answers here.
 6a(i-iv) Condition Flags [5 points] (draw circuits on next page) 6b Result of the ALU when <i>Invert A = 1, Negate B</i> 			[5 points] ge) en <i>Invert A</i> =	1, Negate B	6c (iv) [2 points] For what range(s) of values for (A - B) (before modular arithmetic) does the approach work incorrectly ?
= 1, and Operation ID = 10. [4 points]			10. [4 points]		
					6c (v) [4 points] Draw your circuit for the Less-Than Flag on the next page and indicate the values of <i>Invert</i> , <i>Negate B</i> , and <i>Operation</i> for the operator A < B.
6c (i) [3 points] Three examples of A, B, where sign bit of A-B gives the correct result for A <b.< td=""><td>mples of A, I ect result for</td><td>∃, where sign A<b.< td=""><td>6c(vi) [2 points] Explain why your Less-Than Flag design is correct.</td></b.<></td></b.<>			mples of A, I ect result for	∃, where sign A <b.< td=""><td>6c(vi) [2 points] Explain why your Less-Than Flag design is correct.</td></b.<>	6c (vi) [2 points] Explain why your Less-Than Flag design is correct.
Α	В	A - B	sign(A-B)	ls A < B?	
	1				6d [3 points] Show your Equals Flag design on the next page and indicate the values of <i>Invert</i> , <i>Negate B</i> , and <i>Operation</i> for the operator A == B
bc (II) [1 point] For what range of values for (A - B) (before modular arithmetic) does the approach work correctly?			tic) does the	approach	6e. [3 points] Argue that signed overflow can't affect the Equals Flag.
					(i) If A = B:
					(ii) If $A \neq B^{+}$
6c (iii) [2 points] Two examples of A, B, where sign bit of A-B gives the incorrect result for A <b.< td=""><td>mples of A, B rrect result f</td><td>, where sign or A<b.< td=""><td></td></b.<></td></b.<>			mples of A, B rrect result f	, where sign or A <b.< td=""><td></td></b.<>	
A	В	A - B	sign(A-B)	ls A < B?	

6a(i-iv) Condition Flags, 6c(v) Less-Than Flag, 6d Equals Flag. Label outputs clearly.



Q7 Base64 Encoding [Optional for Fun] *Time spent on Q7:* _____ Show how to encode the hex bytes 49 E2 99 A5 CF 80 21 as Base64 digits:

Base64 representation of the message: