## Program, Application

## Programming Language

Welcome to
CS 240:
Foundations of

## Computer

 Systems
## Compiler/Interpreter

## Operating System

## Instruction Set Architecture

## Microarchitecture

## Digital Logic

Devices (transistors, etc.)
Solid-State Physics

## Today

1 What is CS 240?

2 Why take CS 240?

3 How does CS 240 work?
4 Dive into foundations of computer hardware.

CS 111, 230, 231, 235, 251:

- What can a program do?
- How can a program solve a problem?
- How do you structure a program?
- How do you know it is correct or efficient?
- How hard is it to solve a problem?
- How is computation expressed?
- What does a program mean?

A BIG question is missing...

CS 240: How do computers work?



Algorithm, Data Structure, Application

## Programming Language

Big Idea:

## Compiler/Interpreter

## Operating System

## Instruction Set Architecture

## Microarchitecture

## Digital Logic

Devices (transistors, etc.)
Solid-State Physics

## Big Idea: Abstraction

with a few recurring subplots
Simple, general interfaces:

- Hide complexity of efficient implementation.
- Make higher-level systems easy to build.
- But they are not perfect.

Representation of data and programs

Translation of data and programs

Control flow within/across programs

## 0s and 1s, electricity <br> compilers, assemblers, decoders



ENIAC (Electronic Numerical Integrator and Computer),
First Turing-complete all-electronic programmable digital computer. University of Pennsylvania, 1940s

| 1930s | 1940s | 1950s | 1960s | $1970 s$ | $1980 s$ | $1990 s$ | 2000s |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## program controls general-purpose hardware



Jean Jennings Bartik and Frances Bilas Spence with part of ENIAC.
The programmers of ENIAC were six women.
http://eniacprogrammers.org/, http://sites.temple.edu/topsecretrosies/


Programming 1940s-style with switches and cables.


## machine translates instructions to control flow

Manchester "Baby" SSEM (Small-Scale Experimental Machine), replica first stored-program computer -- University of Manchester (UK), 1948
Tmage: "SSEMM Manchester museum close up" by Parrot of Doom - Own work. Licensed under Creative Commons Attribution-Share Alike 3.0 via Wikimedia Commons - http://commons.wikimedia.org/wiki/File:SSEM_Manchester_museum_close_up.jpg

| $1930 s$ | $1940 s$ | $1950 s$ | $1960 s$ | $1970 s$ | $1980 s$ | $1990 s$ | $2000 s$ | $2010 s$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## PDP-11 "minicomputers"

| $1930 s$ | $1940 s$ | $1950 s$ | $1960 s$ | $1970 s$ | 1980s | 1990s | 2000s | 2010s |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |






| 1930s | 1940s | 1950s | 1960s | 1970s | 1980s | 1990s | 2000s | 2010s |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |


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| $1930 s$ | $1940 s$ | $1950 s$ | $1960 s$ | $1970 s$ | $1980 s$ | $1990 s$ | $2000 s$ | 2010s |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## Modern Computer Organization



| $1930 s$ | $1940 s$ | $1950 s$ | $1960 s$ | $1970 s$ | $1980 s$ | $1990 s$ | 2000s | 2010s |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## Modern Computer Organization



## Processor repeats:

1. fetch instruction
2. fetch data used by instruction
3. execute instruction on data
4. store result or choose next instruction

## Desired computation represented as instructions.

## Hardware/Software Interface

Physical implementation of instructions and resources.

## Computer

Microarchitecture (|lmplementation of ISA)


## Instruction Set Architecture (HW/SW \|nterface)



## Computer

(adds two values and stores the result)

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Instruction Set Architecture specification


| 1930s | 1940s | 1950s | 1960s | $1970 s$ | $1980 s$ | $1990 s$ | $2000 s$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## Assemblers and Assembly Languages

addl \%eax, \%ecx


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Assembly Language specification


| $1930 s$ | $1940 s$ | $1950 s$ | $1960 s$ | $1970 s$ | $1980 s$ | $1990 s$ | $2000 s$ | $2010 s$ |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## Higher-Level Programming Languages



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## Programming Language specification



| 1930s | 1940s | 1950s | 1960s | 1970 s | 1980s | 1990s | 2000s | 2010s |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |

## A-0: first compiler, by Grace Hopper

Early 1950s
Maybe closer to assembler/linker/loader

Later:
B-0 $\rightarrow$ FLOW-MATIC
$\rightarrow$ COBOL, late 50s
Jean Sammet also involved headed first sci comp group at Sperry in the '50s Later first female president of ACM.


- Operating systems
- Virtual machines
- Hypervisors
- Web browsers


## CS 240: a $\begin{aligned} & \text { 3-5.5 weessearn) } \\ & \text { 3-stane }\end{aligned}$

Hardware implementation
From transistors to a simple computer
Hardware-software interface
From instruction set architecture to $C$
Abstraction for practical systems
Memory hierarchy
Operating systems
Higher-level languages

It's fascinating, great for critical thinking.

System design principles apply to software too.

Sometimes system abstractions "leak." Implementation details affect your programs.

## int $\neq$ integer float $\neq$ real

int $x=$...;
$\mathbf{x * x}>=0$ ?

$$
\begin{aligned}
40000 * 40000 & =1600000000 \\
50000 * 50000 & =-1794967296
\end{aligned}
$$

float $a=$..., $b=$..., $c=. . . ;$
$(a+b)+c==a+(b+c)$ ?

$$
\begin{aligned}
(-2.7 e 23+2.7 e 23)+1.0 & = \\
-2.7 e 23+(2.7 e 23+1.0) & =1.0 \\
& =0.0
\end{aligned}
$$

## Reliability?

## Ariane 5 Rocket, 1996

Exploded due to cast of 64-bit floating-point number to 16-bit signed number. Overflow.


## Boeing 787, 2015


"... a Model 787 airplane ... can lose all alternating current (AC) electrical power ... caused by a software counter internal to the GCUs that will overflow after 248 days of continuous power. We are issuing this AD to prevent loss of all AC electrical power, which could result in loss of control of the airplane." --FAA, April 2015

## Arithmetic Performance

## x / 973

x / 1024

## Memory Performance


several times faster due to hardware caches

## Security


e GHOST vulnerability is a buffer overflow condition that can be easily exploited loca motely, which makes it extremely dangerous. This vulnerability is named after the GetHOSTb nction involved in the exploit.


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## Business



A Heart Device Is Found Vulnerable to Hacker Attacks
By BARNABY J. FEDER
To the long list of objects vulnerable to attack by computer hackers, add the human heart.

The threat seems largely theoretical. But a team of computer security researchers plans to report Wednesday that it had been able to gain wireless access to a combination heart defibrillator and pacemaker.


## Why take CS 240?

- Learn how computers execute programs.
- Build software tools and appreciate the value of those you use.
- Deepen your appreciation of abstraction.
- Learn enduring system design principles.
- Improve your critical thinking skills.
- Become a better programmer:
- Think rigorously about execution models.
- Program carefully, defensively.
- Debug and reason about programs effectively.
- Identify limits and impacts of abstractions and representations.
- Learn to use software development tools.
- Foundations for:
- Compilers, security, computer architecture, operating systems, ...
- Have fun and feel accomplished!


## Also: C programming language

- Invented to build UNIX operating system, 1970s
- OS manages hardware, C close to machine model
- Simple pieces look like Java:
- if, while, for, local variables, assignment, etc.
- Other pieces do not:
- structs vs. objects, functions vs. methods
- addresses, pointers
- no array bounds checks
- weak type system
- Important language, still widely used, but many better PL ideas have come along since.


# https://cs.wellesley.edu/~cs240/ 

Everything is here.
Please read it.

